

# Faculty of Science and Information Technology Department of Computer Science and Engineering Course Offer for Fall-2025 Semester

Code	(Batch-70) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
ENG101	Basic Functional English and English Spoken	Null	3
MAT101	Mathematics - I	Null	3
CSE112	Computer Fundamentals	Null	3
CSE115	Introduction to Biology and Chemistry for Computation	Null	3
		Total	12
		Credits	12

Code	(Batch-69) (New Curriculum-3, Effective from Spring 2023	Pre-requisite	Credit
ENG102	Writing and Comprehension ENG1		3
MAT102	Mathematics-II: Calculus, Complex Variables and Linear Algebra		3
PHY101	Physics - I		3
CSE113	Programming and Problem Solving	Null	3
CSE114	Programming and Problem Solving Lab	Null	1.5
		Total Credits	13.5

Code	(Batch-68) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
PHY102	Physics - II	PHY101	3
PHY103	Physics - II Lab	Null	1.5
CSE121	Electrical Circuits	CSE123	3
CSE122	Electrical Circuits Lab CSE		1.5
CSE123	Data Structure	PHY101	3
CSE124	Data Structure Lab	PHY101	1.5
		Total	13.5
		Credits	13.3

Code	(Batch-67) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
AOL101	Art of Living	Null	3
CSE221	Object Oriented Programming	CSE113,	3
05221	o of the oriented 110gramming	CSE213	
CSE222	Object Oriented Programming Lab	CSE114,	1.5
CSEZZZ	CSE222 Object Offented Flogramming Lab		1.5
CSE215	Electronic Devices and Circuits	CSE121	3
CSE216	Electronic Devices and Circuits Lab	CSE122	1.5
BNS101	Bangladesh Studies	Null	3
DIVISIOI	(History of Independence and Contemporary Issues)	INUII	3
	Total		15
		Credits	13

Code	(Batch-66) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
CSE223	Digital Logic Design  CSE- CSE-		3
CSE224	Digital Logic Design Lab	CSE-122, CSE-216	1.5
CSE225	Data Communication	CSE-112	3
CSE227	Systems Analysis and Design	CSE-113, CSE-212	3
CSE228	Theory of Computation	CSE-212	3
		Total Credits	13.5

Code	(Batch-65) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
STA101	Statistics and Probability	MAT-211	3
CSE317	Microprocessor and Microcontrollers	CSE-223	3
CSE313	Compiler Design	CSE-225	3
CSE314	Compiler Design Lab	None	1.5
ACT327	Financial and Managerial Accounting N		3
		Total	13.5
		Credits	13.3

Code	(Batch-64) (New Curriculum-3, Effective from Spring 2023) Pre-requisite		Credit	
CSE315	Software Engineering CSE-2		3	
CSE316	Artificial Intelligence None		3	
CSE323	Operating Systems	Systems CSE-221		
CSE324	Operating Systems Lab CSE-2		1.5	
CSE328	Introduction to Data Science None		3	
		Total	13.5	
		Credits	13.3	

Code	(Batch-63) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE331	Compiler Design	CSE231	3
CSESSI	Compiler Design	CSE131	3
CSE332	Compiler Design Lab	CSE232	1
CSE334	4 Research and Innovation in CSE No		1
CSE415	Web Engineering	CSE333	2
CSE416	Web Engineering Lab CSE		2
CSE445	Natural Language Processing	None 3	
CSE499	FYDP (Title Defense) None		1
		Total	13
		Credits	13

Code	(Batch-62) (New Curriculum-2, Effective from Spring 2022)	<b>Pre-requisite</b>	Credit
CSE413	SE412 Makila Application Design		1
CSE413	Mobile Application Design	CSE214	1
CSE414	Mobile Application Design Lab	CSE215	2
CCE411	CCE411 ArtiCaial Intallianna		2
CSE411	Artificial Intelligence	CSE325	3

CSE412	Artificial Intelligence Lab	CSE232 CSE325	1
CSE431	Social and Professional Issues in Computing	None	3
		CSE499:	
CSE499	FYDP (Phase-I, continued from Level 4 Term 1)	Title	2
		Defense	
		Total	12
		Credits	12

Code	(Batch-61) (New Curriculum-2, Effective from Spring 2022)	<b>Pre-requisite</b>	Credit
		CSE122	
CSE421	Computer Graphics	CSE221	2
		CSE231	
		CSE123	
CSE422	Computer Graphics Lab	CSE222	2
		CSE232	
CSE431	Social and Professional Issues in Computing	None	3
CSE423	Information Security	None	3
		CSE499:	
CSE400	EVDD (Dhasa II, continued from Loyal 4 Tarm 2)	Project /	3
CSE499	FYDP (Phase-II, continued from Level 4 Term 2)	Internship	3
		(Phase I)	
	To		13
		Credits	13

**Dr. Sheak Rashed Haider Noori**Professor & Head, Department of CSE
Daffodil International University



## Faculty of Science and Information Technology Department of Computer Science and Engineering NOTICE for the Registration of Fall-2025 Semester

It is hereby notified to all the students of Dept. of Computer Science and Engineering that their course registration for **Fall-2025 Semester** will be held according to the following schedule. The Course Advisor will help the students in the registration process. Without the course registration, students will not be allowed in the class and their name and ID will not be recorded in the attendance register.

Registration Date: 7th, 8th, 9th, 10th, 11th, 13th and 14th September 2025.

Time: 10:00am-1:00pm and 2:00pm - 3:30pm

Contact your respective Batch Advisor for completing your Course Registration. Retake subjects have to be registered in the Retake Section (if it is available). Old Syllabus students are asked to register in the Old Syllabus Course's section (if it is available). Steps to be followed-

- 1. All the students are suggested to confirm Registration Clearance green tick in the student portal by paying 1<sup>st</sup> installment money to DIU accounts section, detail schedule of collection already mailed to you and posted on Online Notice Board.
- 2. Students are also suggested to clear all the previous dues before the date of the registration.
- 3. Students are advised to follow the motto of "First Come, First Section" to confirm their course registration.
- 4. Students must follow their respective syllabus / course structure (Course code/title) which is available in department notice board/ online notice board/ Dept. Facebook page and Digital Notice Board.

Classes of Fall-2025 semester will start from 10<sup>th</sup> September, 2025.

No Add/Drop will be allowed after the last date of Registration. Retake subjects should be registered in Retake Section (if it is available).

Dr. Sheak Rashed Haider Noori

Professor and Head Department of CSE Daffodil International University

# **Semester Schedule**

Event Details	Fall 2025 Semester
Advising & Registration	07 <sup>th</sup> September 2025 - 14 <sup>th</sup> September 2025
Orientation Program (Newly Admitted Students)	13 <sup>th</sup> September 2025
Classes Start	10 <sup>th</sup> September 2025
First Class Test	4 <sup>th</sup> October 2025- 7 <sup>th</sup> October 2025
Second Class Test	15 <sup>th</sup> October 2025- 21 <sup>st</sup> October 2025
Industry Related Activities	20 <sup>th</sup> October 2025- 28 <sup>th</sup> October 2025
Mid Examination Week	29 <sup>th</sup> October 2025- 05 <sup>th</sup> November 2025
Industry Related Activities	08 <sup>th</sup> November 2025- 16 <sup>th</sup> November 2025
Third Class Test	1st December 2025 – 07th December 2025
Final Exam	17 <sup>th</sup> December 2025 – 24 <sup>th</sup> December 2025
Last Date of Grade Submission	29 <sup>th</sup> December 2025
Publication of Semester Result	30 <sup>th</sup> December 2025
Semester Break	29 <sup>th</sup> December 2025 – 31 <sup>st</sup> December 2025
Advising & Registration for Next Semester	03 <sup>th</sup> January 2026- 12 <sup>th</sup> January 2026
Class Start of Next Semester	07 <sup>th</sup> January 2026

# Daffodil International University Department of Computer Science and Engineering Mid Examination Routine, Fall 2025

		Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
	29/10/2025 Wednesday	CSE431 (D) Social and Professional Issues in Computing 300 CSE123	(Batch-61)	CSE315 Software Engineering 750 CSE225	(Batch-64)	CSE431 Social and Professional Issues in Computing 1100 CSE113	(Batch-62)
		Data Structure (D) 960	(Batch-68)	Data Communication 750	(Batch-66)	Programming and Problem Solving 750	(Batch-69)
		Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
	30/10/2025	CSE331 Compiler Design 900	(Batch-63)	CSE221 Object Oriented Programming 750	(Batch-67)		
	Thursday	STA101 Statistics and Probability 960	(Batch-65)	CSE112 Computer Fundamentals 800	(Batch-70)		
		Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
	01/11/2025 Saturday	CSE328 Introduction to Data Science 1100	(Batch-64)	CSE228 Theory of Computation 960	(Batch-66)	MAT102 Mathematics-II: Calculus, Complex Variables and Linear Algebra 1100	(Batch-69)
		CSE313 Compiler Design 750	(Batch-65)	CSE215 Electronic Devices and Circuits 960	(Batch-67)		
		Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
		CSE423 Information Security 800	(Batch-61)	CSE445 Natural Language Processing 750	(Batch-63)	CSE445 Mathematics - I 750	(Batch-70)
	02/11/2025 Sunday	CSE411 Artificial Intelligence 900	L-4,T-1 (Batch-62)	PHY102 Physics - II 1100	L-1, T-2 (Batch-68)		

	Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
	CSE316 Artificial Intelligence 1000	(Batch-64)	CSE223 (D) Digital Logic Design 750	(Batch-66)	PHY101 Physics - I 960	(Batch-68)
03/11/2025 Monday	CSE317 Microprocessor and Microcontrollers 750	(Batch-65)	BNS101 Bangladesh Studies (History of Independence and Contemporary Issues) 750	(Batch-67)	CSE 115 Introduction to Biology and Chemistry for Computation 1000	(Batch-70)
	Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
04/11/2025	CSE421 Computer Graphics 900	(Batch-61)	CSE415 Web Engineering 400	(Batch-63)		
04/11/2025 Tuesday	CSE413  Mobile Application Design 750	(Batch-62)	CSE121 Electrical Circuits 800	(Batch-68)		
	Slot A: 09:00 am – 10:30 am	Batch	Slot B: 11:30 am – 01:00 pm	Batch	Slot C: 02:00 pm – 03:50 pm	Batch
	CSE323 Operating Systems 750	(Batch-64)	CSE227 Systems Analysis and Design 960	(Batch-66)	ENG102 Writing and Comprehension 960	(Batch-69)
05/11/2025 Wednesday	ACT327 Financial and Managerial Accounting 400	(Batch-65)	AoL101 Art of Living 1100	(Batch-67)	ENG101  Basic Functional English and English Spoken 750	(Batch-70)

#### **Instructions for Students**

- 1. No student will be allowed to sit for examination without clearing their payment to the university or Showing proper document.
- 2. Each student is instructed to collect his/her Admit Card or Payment Document from Accounts Section and bring it to examination hall. No student will be allowed to sit for Examination without Admit Card.
  - 3. It is mandatory to attend the examination according to the sitting arrangement; if not, students will be strictly redirected to follow the sitting arrangement.

    4. Students are strongly advised to write their teachers' initials (for respective courses & sections) correctly to avoid inconvenient circumstances.
    - 5. ID card is mandatory to enter into exam hall.
    - 6. Hang your student ID card with DIU Ribbon with clear visibility.

Firoz Hasan

Firoz Hasan (Contact Point)
Shirin Sultana (Member)
Lamia Rukhsara (Member)
Mr. Mayen Uddin Mojumdar(Member)
Rahmatul Kabir Rasel Sarker (Member)
Md. Mahedi Hassan (Member)
Md Umaid Hasan (Member)



Md. Abbas Ali Khan Convener Examination Committee, FSIT Md Jakaria Zobair (Member) Ms. Fatema Tuj Johora(Member) Mr. Md. Abdullah Al Mamaun (Member) Mr. Md. Monarul Islam (Member) Mr. Sakib Mahmood Chowdhury(Member)



# Daffodil International University Faculty of Science and Information Technology Department of Computer Science and Engineering Registration Guidelines for Students

Registration Motto: "First Come, First Section."

- 1. Complete your **teaching evaluation** first. Pay the required amount to confirm your registration clearance by checking for a **green tick** in the registration section of your student portal before meeting with your batch advisor for course registration.
- 2. Find your assigned batch advisor to register for courses. Course registration will follow the 'First Come, First Section' policy. Do not attempt to register with an advisor not assigned to you.
- 3. Register only for your offered courses. Do not enroll in any advanced courses.
- 4. Check the **exam schedule** to avoid potential overlaps when registering to retake courses. No overlap exams will be arranged from Fall 2024 onwards.
- **4.** You are advised to register for backlog courses (those with an 'F' grade, 'I' grade, or 'W' grade) first, before enrolling in regularly offered courses.
- 5. A student can retake a course if the grade is below B+.
- 6. Do not register for any courses if the prerequisite courses have not been completed.
- 7. Write the appropriate course names and course codes (as per the course offer) on the registration clearance slip and submit it to your batch advisor for registration.
- 8. Students following the old syllabus (with IDs before 191), students of Spring 2019 syllabus and students of Spring 2022 syllabus courses which are not offered yet, are advised to check courses availability then check each course's course code, title, credit, and section before completing their registration.
- 9. Optional courses are batch-specific. Ensure you check your optional courses before registering.

- 10. Register on the specific day selected for you by the department.
- 11. After your advisor completes your course registration, log in to your student portal to verify that all courses have been registered correctly according to the course offerings.
- 12. Once you have registered, changing sections will not be allowed.
- 13. We will strictly maintain section sizes: 50 students for theory courses and 25 for lab courses. Therefore, you are advised to register for your courses within the scheduled dates.
- 14. Check your university email and online notice board regularly for any updates or changes regarding registration.
- 15. Contact your Batch Advisor immediately if you have any issues during registration.
- 16. Familiarize yourself with the academic calendar to avoid missing important dates and deadlines.
- 17. Regularly check course availability, particularly if you plan to retake a course.

Dr. Sheak Rashed Haider Noori

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## **Decision on Automated Section Creation & Opening System**

As per the decision of the of the DIU Management Committee, an Automated Section Creation & Opening System has been implemented to enhance operational efficiency and ensure standardized management of course sections.

### The system shall function under the following guidelines:

- 1) When the courses will be offered into the system, a section will be created automatically with a capacity of 50 students for theory courses and 25 students for lab courses (already implemented).
- 2) When the section will be filled up by the required number of students (45 students for theory courses and 22 students for lab courses), the system will automatically generate a new section.
- 3) Departments will no longer have the ability to create sections manually.