

# Final Examination, Faculty of Engineering, Fall – 2021

Date: 18/12/2021 Slot: C (2:30-4:30PM)

## MASTER SEAT PLAN

<u>ROOM # 809</u>				
<u>COL-1</u>	<u>COL-2</u>	<u>COL-3</u>	<u>COL-4</u>	<u>COL-5</u>
EEE-225 Digital Electronics Sec-D (DAS)		EEE-225 Digital Electronics Sec-D (DAS)		EEE-225 Digital Electronics Sec-D (DAS)
8	8	8	8	8

<u>ROOM # 810</u>					
<u>COL-1</u>	<u>COL-2</u>	<u>COL-3</u>	<u>COL-4</u>	<u>COL-5</u>	<u>COL-6</u>
EEE-225 Digital Electronics Sec-C (DAS)	EEE-121 Electrical Circuits II (MSA) Batch L2-T1	EEE-225 Digital Electronics Sec-C (DAS)	EEE-121 Electrical Circuits II (MSA) Batch L2-T1	EEE-225 Digital Electronics Sec-C (DAS)	EEE-121 Electrical Circuits II (MSA) Batch L2-T1
6	6	6	6	6	6

<u>ROOM # 811</u>							
<u>COL-1</u>	<u>COL-2</u>	<u>COL-3</u>	<u>COL-4</u>	<u>COL-5</u>	<u>COL-6</u>	<u>COL-7</u>	<u>COL-8</u>
EEE-225 Digital Electronics Sec-D (DAS)	CHE-111 Chemistry (MMR)	EEE-225 Digital Electronics Sec-D (DAS)	CHE-111 Chemistry (MMR)	EEE-225 Digital Electronics Sec-D (DAS)	EEE-225 Digital Electronics Sec-D (DAS)	CHE-111 Chemistry (MMR)	EEE-225 Digital Electronics Sec-D (DAS)
6	6	6	6	6	6	5	5

