Daffodil International University (DIU)

Daffodil Smart City, Birulia Savar, Dhaka-1216, Bangladesh



Academic Calendar 2024

Event Details	Spring 2024	Fall 2024
Advising and Registration	8 January - 17 January 2024	1 July - 10 July 2024
Classes Start	18 January 2024	11 July 2024
Class Test - 1	1st Week of February No. of classes before CT-1 (3 Weeks)	1 st Week of August No. of classes before CT-1 (3 Weeks)
Class Test - 2	4 th Week of February No. of classes before CT-2 (2 Weeks)	1 st Week of September No. of classes before CT-2 (3 Weeks)
Midterm Examination	2 March —16 March 2024 (2 Weeks) No. of classes before IHidterm (7 Weeks)	7 September - 21 September 2024 (2 Weeks) No. of classes before Midterm (8 Weeks)
Class Starting After Midterm Exam	18 March 2024	22 September 2024
Eid-ul-Fitr Vacation*	9 April — 11 April 2024	
Class Test - 3	4 th Week of April No. of classes before CT-3 (5 Weeks)	4 th Week of October No. of classes before CT-3 (5 Weeks)
Final Examination	28 May - 10 June 2024 (2 Weeks) ho. of classes before Final (10 Weeks)	3 December —17 December 2024 (2 Weeks) No. of classes before Final (10 Weeks)
Last date of Grade Submission	15 June 2024	21 December 2024
Eid-ul-Adha Vacation*	16 June -18 June 2024	
Publication of Semester Results	22 June 2024	24 December 2024
Semester Break	23 June -25 June 2024	26 December -29 December 2024
Course File Preparation and Faculty Training	26 June -30 June 2024	30 December 2024 -5 January 2025
Advising and Registration of Next Semester	1 July - 10. July 2024	1 January - 10 January 2025
Classes Start of Next Semester	11 July 2024	11 January 2025

*Dependent on moon sighting

* The schedule may be subject to change based on demand or situational factors.



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Daffodil International University



Faculty of Science and Information Technology Department of Computer Science and Engineering Course Offer for Spring 2024 Semester

Code	L-1, T-1 (Batch-66) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
ENG101	Basic Functional English and English Spoken	Null	3
MAT101	Mathematics - I	Null	3
PHY101	Physics-I	Null	3
CSE112	Computer Fundamentals	Null	3
CSE113	Programming and Problem Solving	Null	3
CSE114	Programming and Problem Solving Lab	Null	1.5
CSE115	Introduction to Biology and Chemistry for Computation	Null	3
		Total Credits	19.5

Code	L-1, T-2 (Batch-65) (New Curriculum-3, Effective from Spring 2023	Pre-requisite	Credit
ENG102	Writing and Comprehension	ENG-101	3
MAT102	Mathematics-II: Calculus, Complex Variables and Linear Algebra	MAT-101	3
PHY 102	Physics - II	PHY-101	3
PHY 103	Physics - II Lab	PHY-101	1.5
CSE121	Electrical Circuits	PHY-101	3
CSE122	Electrical Circuits Lab	PHY-101	1.5
CSE-123	Data Structure	CSE-113	3
CSE-124	Data Structure Lab	CSE-114	1.5
		Total Credits	19.5

Code	L-2, T-1 (Batch-64) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
MAT211	Engineering Mathematics	MAT102	3
CSE212	Discrete Mathematics	Null	3
CSE213	Algorithms	CSE113	3
CSE214	Algorithms Lab	CSE114	1.5
CSE215	Electronic Devices and Circuits	CSE121	3
CSE216	Electronic Devices and Circuits Lab	CSE122	1.5
BNS101	"Bangladesh Studies (History of Independence and	Null	3
	Contemporary Issues)"		
AOL101	Art of Living	Null	3
		Total Credits	21

Code	L-2, T-2 (Batch-63) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE235	Numerical Methods	MAT121,	3
		MAT211	
CSE223	Digital Electronics	CSE131	1
CSE224	Digital Electronics Lab	CSE131	2
CSE225	Data Communication	CSE131,	3
		CSE223	
STA227	Statistics and Probability	MAT111	3
CSE231	Algorithms	CSE122	3
CSE232	Algorithm Lab	CSE123	1

CSE236	Software Project II	Null	1
		Total Credits	17

Code	L-3, T-1 (Batch-62) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE221	Object Oriented Programming II	CSE231	1
CSE222	Object Oriented Programming II Lab	CSE232	2
CSE236	Software Project II	Null	1
CSE235	Numerical Methods	MAT121, MAT211	3
ECO237	Economics	Null	3
CSE231	Algorithm	CSE122	3
CSE232	Algorithm Lab	CSE123	1
CSE335	Computer Architecture and Organization	CSE314	3
		Total Credits	17

Code	L-3, T-1 (Batch-61) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE221	Object Oriented Programming II	CSE231	1
CSE222	Object Oriented Programming II Lab	CSE232	2
CSE223	Digital Electronics	CSE131	1
CSE224	Digital Electronics Lab	CSE131	2
CSE321	System Analysis and Design	CSE214	3
ACT322	Financial and Managerial Accounting	Null	2
CSE334	Research and Innovation in CSE	Null	1
CSE315	Introduction to Data Science	Null	3
CSE316	Software Project III	Null	1
		Total Credits	16

Code	L-3, T-2 (Batch-60) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE321	Data Mining and Machine Learning	CSE311,	2
	e e	CSE312	
CSE322	Data Mining and Machine Learning Lab	CSE311,	2
		CSE312	
CSE323	Operating Systems	CSE 213, CSE	1
	1 0 7	231	
CSE324	Operating Systems Lab	CSE 213, CSE	2
	1 0 1	231	
CSE325	System Analysis and Design	CSE214	3
ECO321	Economics	Null	2
CSE326	Research and Innovation	Null	1
CSE317	Software Project V	Null	1
CSE334	Pervasive Computing	CSE214	1
CSE335	Pervasive Computing and Mobile App Development Lab	CSE214	2
		Total Credits	17

Code	L-4, T-1 (Batch-59) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE331	Compiler Design	CSE213, CSE221	2
CSE332	Compiler Design Lab	CSE213, CSE221	2
CSE333	Software Engineering	CSE214	3
CSE334	Pervasive Computing	CSE214	1
CSE335	Pervasive Computing and Mobile App Development Lab	CSE214	2

CSE336	Software Project VI	Null	1
CSE411	Computer Architecture and Organization	CSE314	3
CSE412	Big Data and IoT	CSE232	1
CSE413	Big Data and IoT Lab	CSE232	2
		Total Credits	17

Code	L-4, T-1 (Batch-58) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE411	Computer Architecture and Organization	CSE314	3
CSE412	Big Data and IoT	CSE232	1
CSE413	Big Data and IoT Lab	CSE232	2
CSE414	Web Engineering	CSE214	1
CSE415	Web Engineering Lab	CSE214	2
CSE421	Computer Graphics	MAT211, CSE213	1
CSE422	Computer Graphics Lab	CSE421	2
CSE445	Natural Language Processing (Elective-1)	Null	3
CSE499	Project / Internship (Phase I, to be completed in Level-4	Null	2
	Term-2)		
		Total Credits	17

Code	L-4, T-2 (Batch-57) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE423	Information Security	Null	3
CSE498	Social and Professional Issues in Computing	Null	3
CSE411	Computer Architecture and Organization	CSE314	3
CSE434	Human Computer Interaction (Elective -II)	Null	3
CSE499	Project / Internship (Phase II: (4 Credits)	CSE499:	4
		Project /	
		Internship	
		(Phase I)	
		Total Credits	16

Code	L-4, T-2 (Batch-56) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE412	Big Data and IoT	CSE232	1
CSE413	Big Data and IoT Lab	CSE232	2
CSE427	Digital Image Processing (Elective -II)	Null	3
CSE499	Project / Internship (Phase II): (4 Credits)	CSE499:	4
		Project /	
		Internship	
		(Phase I)	
		Total Credits	10

Professor Dr. Sheak Rashed Haider Noori

Head, Department of CSE Daffodil International University

Registration Guidelines for Students

Contact to your respective Batch Advisor for Completing course registration.

- 1. Bring Registration Clearance and Find the specific time of your Batch Advisor before come to registration.
- 2. Find your "Batch Advisor" to register the courses. Do not attempt for other Teachers.
- 3. Register your offered courses. Do not go for extra/advance courses.
- 4. You are advised to register backlog courses (like "F" grade, "I" grade and "W" grade) first then regular offered courses.
- 05. A student can take a course as retake if its grade be **below B+**.
- 06. Do not take any courses if pre-requisite courses were not completed.
- 07. Write the appropriate (as per the course offer) courses name and course Code in the registration Clearance slip and give it to the Batch Advisor for Registration.
- 08. Old syllabus students (before 191 initial id students) are advised to check course code, title, credit and section of the individual course before completing their registration.
- 09. Optional courses are batch specific. Check your optional course before Register.
- 10. Come on that particular day selected for you by the authority.
- 11. Join in the first day of class starts.
- 12. We will maintain the section size(50) strictly, therefore you advised to register your courses within the scheduled date.



Faculty of Science and Information Technology Department of Computer Science and Engineering NOTICE for the Registration of Spring-2024 Semester

It is hereby notified to all the students of Dept. of Computer Science and Engineering that their course registration for **Spring-2024 Semester** will be held according to the following schedule. The Course Advisor will help the students in the registration process. Without the course registration, students will not be allowed in the class and their name and ID will not be recorded in the attendance register.

Registration Date: 8th, 9th, 10th, 11th, 13th, 14th, 15th, 16th and 17th January 2024.

Time: 9:00am-1:00pm and 2:00pm - 4:00pm

Contact your respective Batch Advisor for completing your Course Registration. Retake subjects (GED) should be registered in Retake Section if it is available. Old Syllabus students are asked to register in the Old Syllabus Course's section (if it is available).

Steps to be followed-

- 1. All the students are suggested to collect the Registration Clearance Form from the Accounts Section by paying 1st installment money to DIU accounts section, detail schedule of collection already mailed to you and posted on Online Notice Board.
- 2. Students are also suggested to clear all the previous dues before the date of the registration.
- 3. Students are advised to follow the same procedure like previous semester to confirm their registration.
- 4. Students must follow their respective syllabus / course structure (Course code/title) which is available in department notice board/ online notice board/ Dept. Facebook page and Digital Notice Board.

Classes of Spring-2024 semester will start from 18th January, 2024.

No Add/Drop will be allowed after the last date of Registration. Retake subject (GED) should be registered in Retake Section (if it is available).

Professor Dr. Sheak Rashed Haider Noori

Head
Department of CSE
Daffodil International University



Daffodil International University Department of Computer Science and Engineering Tentative Examination Routine, Spring 2024

	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 1	ENG-101 Basic Functional English and English Spoken	L-1, T-1 (Batch-66)	CSE221 Object Oriented Programming II	L-3, T-1 (Batch-61)	CSE221 Object Oriented Programming II	L-3, T-1 (Batch-62)
DATE	CSE-123 Data Structure	L-1, T-2 (Batch-65)				
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 2	CSE-212 Discrete Mathematics	L-2, T-1 (Batch-64)	CSE225 Data Communication	L-2, T-2 (Batch-63)	CSE321 Data Mining and Machine Learning	L-3,T-2 (Batch-60)
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 3	ENG-102 Writing and Comprehension	L-1, T-2 (Batch-65)	CSE-113 Programming and Problem Solving	L-1, T-1 (Batch-66)	CSE421 Computer Graphics	L-4,T-1 (Batch-58)
DAI - 0	CSE331 Compiler Design	L-4,T-1 (Batch-59)	CSE434 Human Computer Interaction	L-4,T-2 (Batch-57)		
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 4	CSE-213 Algorithms	L-2, T-1 (Batch-64)	CSE231 Algorithms	L-2, T-2 (Batch-63)	CSE323 Operating Systems	L-3,T-2 (Batch-60)
			CSE231 Algorithm	L-3, T-1 (Batch-62)		



Daffodil International University Department of Computer Science and Engineering Tentative Examination Routine, Spring 2024

Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 5	CSE-112 Computer Fundamentals	L-1, T-1 (Batch-66)	ACT322 Financial and Managerial Accounting	L-3, T-1 (Batch-61)	CSE333 Software Engineering	L-4,T-1 (Batch-59)
DAI = 0	CSE414 Web Engineering	L-4,T-1 (Batch-58)				
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
	CSE-121 Electrical Circuits	L-1, T-2 (Batch-65)	AOL-101 Art of Living	L-2, T-1 (Batch-64)	CSE423 Information Security	L-4,T-2 (Batch-57)
DAY - 6	STA227 Statistics and Probability	L-2, T-2 (Batch-63)	ECO237 Economics	L-3, T-1 (Batch-62)		
	ECO321 Economics	L-3,T-2 (Batch-60)				
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
			CSE223 Digital Electronics	L-3, T-1 (Batch-61)	CSE223 Digital Electronics	L-2, T-2 (Batch-63)
DAY - 7					CSE412 Big Data and IoT	L-4,T-1 (Batch-59)
DAT - 7					CSE412 Big Data and IoT	L-4,T-1 (Batch-58)
					CSE412 Big Data and IoT	L-4,T-2 (Batch-56)
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
	PHY- 102 Physics - II	L-1, T-2 (Batch-65)	MAT-101 Mathematics - I	L-1, T-1 (Batch-66)		
DAY - 8			BNS-101 Bangladesh Studies (History of Independence and Contemporary Issues)	L-2, T-1 (Batch-64)		



Daffodil International University Department of Computer Science and Engineering Tentative Examination Routine, Spring 2024

Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
	CSE335 Computer Architecture and Organization	L-3, T-1 (Batch-62)	CSE321 System Analysis and Design	L-3, T-1 (Batch-61)		
DAY - 9	CSE411 Computer Architecture and Organization	L-4,T-1 (Batch-59)	CSE325 System Analysis and Design	L-3,T-2 (Batch-60)		
27.1. 0	CSE411 Computer Architecture and Organization	L-4,T-2 (Batch-57)				
	CSE411 Computer Architecture and Organization	L-4,T-1 (Batch-58)				
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 10	MAT-102 Mathematics-II: Calculus, Complex Variables and Linear Algebra	L-1, T-2 (Batch-65)	CSE-115 Introduction to Biology and Chemistry for Computation	L-1, T-1 (Batch-66)	CSE427 Digital Image Processing	L-4,T-2 (Batch-56)
			MAT-211 Engineering Mathematics	L-2, T-1 (Batch-64)		
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 11	CSE235 Numerical Methods	L-2, T-2 (Batch-63)	CSE315 Introduction to Data Science	L-3, T-1 (Batch-61)	CSE 445 Natural Language Processing	L-4,T-1 (Batch-58)
	CSE235 Numerical Methods	L-3, T-1 (Batch-62)				
Date	Slot A: 9.00 – 11.00 am	Batch	Slot B: 12.00 – 02.00 pm	Batch	Slot C: 03.00 – 05.00 pm	Batch
DAY - 12	CSE334 Pervasive Computing	L-4,T-1 (Batch-59)	PHY-101 Physics-I	L-1, T-1 (Batch-66)	CSE498 Social and Professional Issues in Computing	L-4,T-2 (Batch-57)
	CSE334 Pervasive Computing	L-3,T-2 (Batch-60)	CSE-215 Electronic Devices and Circuits	L-2, T-1 (Batch-64)		

Instructions for Students

1. No student will be allowed to sit for examination without clearing their payment to the university or Showing proper document.

- 2. Each student is instructed to collect his/her Admit Card or Payment Document from Accounts Section and bring it to examination hall. No student will be allowed to sit for Examination without Admit Card.

 3. It is mandatory to attend the examination according to the sitting arrangement; if not, students will be strictly redirected to follow the sitting arrangement.
 - 4. Students are strongly advised to write their teachers' initials (for respective courses & sections) correctly to avoid inconvenient circumstances.
 - 5. ID card is mandatory to enter into exam hall.

6. Hang your student ID card with DIU Ribbon with clear visibility.

Dewan Mamun Raza (Convener) Md. Abbas Ali Khan (Member) Mushfiqur Rahman (Member) Shyala Sharmin (Member) Sharmin Akter (Member) Lamia Rukhsara (Member)

Shah Md. Tanvir Siddiquee Chairman Examination Committee, FSIT Firoz Hasan (Member)
Aynul Hasan Nahid (Member)
Md. Assaduzzaman (Member)
Amir Sohel (Member)
Shirin Sultana (Member)
Mst. Sharmin Akter (Member)