Faculty of Science and Information Technology
Department of Computer Science and Engineering
Course Offer for Fall-2024 Semester

Code	L-1, T-1 (Batch-67) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
ENG101	Basic Functional English and English Spoken	Null	3
MAT101	Mathematics - I	Null	3
PHY101	Physics-I	Null	3
CSE112	Computer Fundamentals	Null	3
CSE113	Programming and Problem Solving	Null	3
CSE114	Programming and Problem Solving Lab	Null	1.5
CSE115	Introduction to Biology and Chemistry for Computation	Null	3
		Total Credits	**19.5**

Code	L-1, T-2 (Batch-66) (New Curriculum-3, Effective from Spring 2023	Pre-requisite	Credit
ENG102	Writing and Comprehension	ENG101	3
MAT102	Mathematics-II: Calculus, Complex Variables and Linear Algebra	MAT101	3
PHY 102	Physics - II	PHY101	3
PHY 103	Physics - II Lab	PHY101	1.5
CSE121	Electrical Circuits	PHY101	3
CSE122	Electrical Circuits Lab	PHY101	1.5
CSE123	Data Structure	CSE113	3
CSE124	Data Structure Lab	CSE114	1.5
		Total Credits	**19.5**

Code	L-2, T-1 (Batch-65) (New Curriculum-3, Effective from Spring 2023)	Pre-requisite	Credit
MAT211	Engineering Mathematics	MAT102	3
CSE212	Discrete Mathematics	Null	3
CSE213	Algorithms	CSE123	3
CSE214	Algorithms Lab	CSE124	1.5
CSE215	Electronic Devices and Circuits	CSE121	3
CSE216	Electronic Devices and Circuits Lab	CSE122	1.5
BNS101	"Bangladesh Studies (History of Independence and Contemporary Issues)"	Null	3
AOL101	Art of Living	Null	3
		Total Credits	**21**

Code	L-2, T-2 (Batch-64) (New Curriculum-2, Effective from Spring 2023)	Pre-requisite	Credit
CSE221	Object Oriented Programming	CSE113, CSE213	3
CSE222	Object Oriented Programming Lab	CSE114, CSE214	1.5
CSE223	Digital Logic Design	CSE121, CSE215	3

CSE224	Digital Logic Design Lab	CSE122, CSE216	1.5
CSE225	Data Communication	CSE112	3
CSE226	Numerical Methods	MAT211	3
CSE227	Systems Analysis and Design	CSE113, CSE212	3
CSE228	Theory of Computation	CSE212	3
		Total Credits	**21**

Code	L-3, T-1 (Batch-63) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE221	Object Oriented Programming II	CSE231	1
CSE222	Object Oriented Programming II Lab	CSE221, 232	2
CSE233	Embedded Systems and IoT	CSE132, CSE223	2
CSE234	Embedded Systems and IoT Lab	CSE233	2
CSE311	Database Management System	CSE134, CSE221	3
CSE312	Database Management System Lab	CSE311	1
CSE313	Computer Networks	CSE221	3
CSE314	Computer Networks Lab	CSE313	1
CSE315	Introduction to Data Science	Null	3
CSE316	Software Project III	Null	1
		Total Credits	**19**

Code	L-3, T-1 (Batch-62) (New Curriculum-2, Effective from Spring 2022)	Pre-requisite	Credit
CSE233	Embedded Systems and IoT	CSE132, CSE223	2
CSE234	Embedded Systems and IoT Lab	CSE233	2
CSE311	Database Management System	CSE134, CSE221	3
CSE312	Database Management System Lab	CSE311	1
CSE313	Computer Networks	CSE221	3
CSE314	Computer Networks Lab	CSE313	1
CSE315	Introduction to Data Science	Null	3
ACT322	Financial and Managerial Accounting	Null	2
CSE316	Software Project III	Null	1
		Total Credits	**18**

Code	L-3, T-2 (Batch-61) (New Curriculum-1, Effective from Spring 2022)	Pre-requisite	Credit
CSE311	Database Management System	CSE134, CSE221	3
CSE312	Database Management System Lab	CSE311	1
CSE313	Computer Networks	CSE221	3
CSE314	Computer Networks Lab	CSE313	1
CSE323	Operating Systems	CSE213, CSE231	2
CSE324	Operating Systems Lab	CSE323	2
CSE325	Data Mining and Machine Learning	CSE311, CSE312	3
CSE326	Data Mining and Machine Learning Lab	CSE325	1
CSE445	Natural Language Processing (Elective-I)	Null	3
		Total Credits	**19**

Code	L-4, T-1 (Batch-60) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE331	Compiler Design	CSE213, CSE221	2
CSE332	Compiler Design Lab	CSE213, CSE221	2
CSE333	Software Engineering	CSE214	3
CSE336	Software Project VI	Null	1
CSE411	Computer Architecture and Organization	CSE314	3
CSE412	Big Data and IoT	CSE232	1
CSE413	Big Data and IoT Lab	CSE412	2
CSE423	Information Security	Null	3
		Total Credits	**17**

Code	L-4, T-1 (Batch-59) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE414	Web Engineering	CSE232	1
CSE415	Web Engineering Lab	CSE414	2
CSE421	Computer Graphics	MAT211, CSE213, CSE222	1
CSE422	Computer Graphics Lab	CSE421	2
CSE423	Information Security	Null	3
CSE445	Natural Language Processing (Elective-I)	Null	3
CSE499	Project / Internship (Phase I, to be completed in Level-4 Term-3)	Null	2
		Total Credits	**14**

Code	L-4, T-2 (Batch-58) (New Curriculum-1, Effective from Spring 2019)	Pre-requisite	Credit
CSE423	Information Security	Null	3
CSE498	Social and Professional Issues in Computing	Null	3
CSE427	Digital Image Processing (Elective -II)	Null	3
CSE499	Project / Internship (Phase II, continued from Level 4 Term 2)	CSE499: Project / Internship (Phase I)	4
		Total Credits	**13**

Dr. Sheak Rashed Haider Noori
Professor & Head, Department of CSE
Daffodil International University

Daffodil International University
Faculty of Science and Information Technology
Department of Computer Science and Engineering
Registration Guidelines for Students
Registration Motto: "First Come, First Section."

1. Complete your **teaching evaluation** first. Pay the required amount to confirm your registration clearance by checking for a **green tick** in the registration section of your student portal before meeting with your batch advisor for course registration.

2. Find your assigned batch advisor to register for courses. Course registration will follow the **'First Come, First Section'** policy. Do not attempt to register with an advisor not assigned to you.

3. Register only for your offered courses. Do not enroll in any advanced courses.

4. Check the **exam schedule** to avoid potential overlaps when registering to retake courses. No overlap exams will be arranged from Fall 2024 onwards.

4. You are advised to register for backlog courses (those with an 'F' grade, 'I' grade, or 'W' grade) first, before enrolling in regularly offered courses.

5. A student can retake a course if the grade is below B+.

6. Do not register for any courses if the prerequisite courses have not been completed.

7. Write the appropriate course names and course codes (as per the course offer) on the registration clearance slip and submit it to your batch advisor for registration.

8. Students following the old syllabus (with IDs before 191), students of Spring 2019 syllabus and students of Spring 2022 syllabus courses which are not offered yet, are advised to check courses availability then check each course's course code, title, credit, and section before completing their registration.

9. Optional courses are batch-specific. Ensure you check your optional courses before registering.

10. Register on the specific day selected for you by the department.

11. After your advisor completes your course registration, log in to your student portal to verify that all courses have been registered correctly according to the course offerings.

12. Once you have registered, changing sections will not be allowed.

13. We will strictly maintain section sizes: 50 students for theory courses and 25 for lab courses. Therefore, you are advised to register for your courses within the scheduled dates.

14. Check your university email and online notice board regularly for any updates or changes regarding registration.

15. Contact your Batch Advisor immediately if you have any issues during registration.

16. Familiarize yourself with the academic calendar to avoid missing important dates and deadlines.

17. Regularly check course availability, particularly if you plan to retake a course.

Dr. Sheak Rashed Haider Noori
Professor and Head
Department of CSE
Daffodil International University

Faculty of Science and Information Technology
Department of Computer Science and Engineering
NOTICE for the Registration of Fall-2024 Semester

It is hereby notified to all the students of Dept. of Computer Science and Engineering that their course registration for **Fall-2024 Semester** will be held according to the following schedule. The Course Advisor will help the students in the registration process. Without the course registration, students will not be allowed in the class and their name and ID will not be recorded in the attendance register.

Registration Date: 1st, 2nd, 3rd, 4th, 6th, 7th, 8th ,9th and 10th July 2024.

Time: 10:00am-1:00pm and 2:00pm – 3:30pm

Contact your respective Batch Advisor for completing your Course Registration.

Retake subjects have to be registered in the Retake Section (if it is available).

Old Syllabus students are asked to register in the Old Syllabus Course's section (if it is available).

<u>Steps to be followed-</u>

1. All the students are suggested to collect the Registration Clearance Form from the Accounts Section by paying **1st installment money** to DIU accounts section, detail schedule of collection already mailed to you and posted on Online Notice Board.
2. Students are also suggested to clear all the previous dues before the date of the registration.
3. Students are advised to follow the motto of **"First Come, First Section"** to confirm their course registration.
4. **Students must follow their respective syllabus / course structure (Course code/title) which is available in department notice board/ online notice board/ Dept. Facebook page and Digital Notice Board.**

Classes of Fall-2024 semester will start from 11th July, 2024.

No Add/Drop will be allowed after the last date of Registration.
Retake subjects should be registered in Retake Section (if it is available).

Dr. Sheak Rashed Haider Noori
Professor and Head
Department of CSE
Daffodil International University

Daffodil International University
Department of Computer Science and Engineering
Mid Semester Examination Routine, Fall 2024

	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 1			CSE325 Data Mining and Machine Learning 1150	L-3,T-2 (Batch-61)	CSE315 Introduction to Data Science 750	L-3, T-1 (Batch-63)		
			CSE421 Computer Graphics 200	L-4,T-1 (Batch-59)	CSE315 Introduction to Data Science 400	L-3, T-1 (Batch-62)		
					CSE331 Compiler Design 300	L-4,T-1 (Batch-60)		

	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 2			CSE113 Programming and Problem Solving 1000	L-1, T-1 (Batch-67)	CSE212 Discrete Mathematics 850	L-2, T-1 (Batch-65)		
			CSE123 Data Structure 1000	L-1, T-2 (Batch-66)	CSE225 Data Communication 850	L-2, T-2 (Batch-64)		

	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 3	**Retake Batch L1T1**		CSE311 Database Management System 1150	L-3,T-2 (Batch-61)	CSE311 Database Management System 750	L-3, T-1 (Batch-63)		
	Retake Batch L2T1		CSE498 Social and Professional Issues in Computing 250	L-4,T-2 (Batch-58)	CSE311 Database Management System 400	L-3, T-1 (Batch-62)		
					CSE333 Software Engineering 300	L-4,T-1 (Batch-60)		

Daffodil International University
Department of Computer Science and Engineering
Mid Semester Examination Routine, Fall 2024

	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 4			CSE112 Computer Fundamentals 1000	L-1, T-1 (Batch-67)	CSE213 Algorithms 850	L-2, T-1 (Batch-65)		
			MAT102 Mathematics-II: Calculus, Complex Variables and Linear Algebra 1000	L-1, T-2 (Batch-66)	CSE228 Theory of Computation 850	L-2, T-2 (Batch-64)		
	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 5	**Retake Batch L1T2**		CSE313 Computer Networks 1150	L-3, T-2 (Batch-61)	CSE313 Computer Networks 750	L-3, T-1 (Batch-63)		
	Retake Batch L2T2				CSE313 Computer Networks 400	L-3, T-1 (Batch-62)		
					CSE411 Computer Architecture and Organization 300	L-4, T-1 (Batch-60)		
					CSE414 Web Engineering 200	L-4, T-1 (Batch-59)		
	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 6			PHY101 Physics-I 1000	L-1, T-1 (Batch-67)	AOL101 Art of Living 850	L-2, T-1 (Batch-65)		
			CSE121 Electrical Circuits 1000	L-1, T-2 (Batch-66)	CSE223 Digital Logic Design 850	L-2, T-2 (Batch-64)		

Daffodil International University
Department of Computer Science and Engineering
Mid Semester Examination Routine, Fall 2024

	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 7	Retake Batch L3T1		CSE445 Natural Language Processing 1150	L-3,T-2 (Batch-61)	CSE221 Object Oriented Programming II 750	L-3, T-1 (Batch-63)		
	Retake Batch L4T1				ACT322 Financial and Managerial Accounting 400	L-3, T-1 (Batch-62)		
					CSE445 Natural Language Processing 200	L-4,T-1 (Batch-59)		
	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 8			MAT101 Mathematics - I 1000	L-1, T-1 (Batch-67)	MAT211 Engineering Mathematics 850	L-2, T-1 (Batch-65)		
			PHY102 Physics - II 1000	L-1, T-2 (Batch-66)	CSE221 Object Oriented Programming 850	L-2, T-2 (Batch-64)		
	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 9	Retake Batch L3T2		CSE323 Operating Systems 1150	L-3,T-2 (Batch-61)	CSE233 Embedded Systems and IoT 750	L-3, T-1 (Batch-63)		
	Retake Batch L4T2				CSE233 Embedded Systems and IoT 400	L-3, T-1 (Batch-62)		
					CSE423 Information Security 300	L-4,T-1 (Batch-60)		
					CSE423 Information Security 200	L-4,T-1 (Batch-59)		
					CSE423 Information Security 250	L-4,T-2 (Batch-58)		

Daffodil International University
Department of Computer Science and Engineering
Mid Semester Examination Routine, Fall 2024

	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 10			CSE115 Introduction to Biology and Chemistry for Computation 1000	L-1, T-1 (Batch-67)	BNS101 Bangladesh Studies (History of Independence and Contemporary Issues) 850	L-2, T-1 (Batch-65)		
			ENG102 Writing and Comprehension 1000	L-1, T-2 (Batch-66)	CSE227 Systems Analysis and Design 850	L-2, T-2 (Batch-64)		
	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 11	**Retake L1T1+L1T2**		**Retake L2T1+L2T2**		**Retake L3T1+L3T2** **Retake L4T1+L4T2**			
	Slot A: 08:30 am – 10:00 am	Batch	Slot B: 11:00 am – 12:30 pm	Batch	Slot C: 01:30 pm – 03:00 pm	Batch	Slot D: 06:00 pm – 07:30 pm	Batch
Day 12			ENG101 Basic Functional English and English Spoken 1000	L-1, T-1 (Batch-67)	CSE215 Electronic Devices and Circuits 850	L-2, T-1 (Batch-65)		
			CSE412 Big Data and IoT 300	L-4,T-1 (Batch-60)	CSE226 Numerical Methods 850	L-2, T-2 (Batch-64)		
			CSE427 Digital Image Processing 250	L-4,T-2 (Batch-58)				

Instructions for Students

1. No student will be allowed to sit for examination without clearing their payment to the university or Showing proper document.
2. Each student is instructed to collect his/her Admit Card or Payment Document from Accounts Section and bring it to examination hall. No student will be allowed to sit for Examination without Admit Card.
3. It is mandatory to attend the examination according to the sitting arrangement; if not, students will be strictly redirected to follow the sitting arrangement.
4. Students are strongly advised to write their teachers' initials (for respective courses & sections) correctly to avoid inconvenient circumstances.
5. ID card is mandatory to enter into exam hall.
6. Hang your student ID card with DIU Ribbon with clear visibility.

Mushfiqur Rahman (Contact Point)
Md. Abbas Ali Khan (Member)
Shyala Sharmin (Member)
Shirin Sultana (Member)
Sharmin Akter (Member)
Lamia Rukhsara (Member)
Mst. Sharmin Akter (Member)

Shah Md. Tanvir Siddiquee
Chairman
Examination Committee, FSIT

Firoz Hasan (Member)
Aynul Hasan Nahid (Member)
Mayen Uddin Mojumdar (Member)
Rahmatul Kabir Rasel Sarker (Member)
Md. Mahedi Hassan (Member)
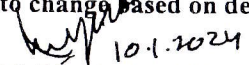Md Umaid Hasan (Member)
Md Jakaria Zobair (Member)

Daffodil International University (DIU)

Daffodil Smart City, Birulia Savar, Dhaka-1216, Bangladesh

Academic Calendar 2024

Event Details	Fall 2024
Advising and Registration	1 July - 10 July 2024
Classes Start	11 July 2024
Orientation Program	20 July 2024
Class Test - 1	1st Week of August No. of classes before CT-1 (3 Weeks)
Class Test - 2	1st Week of September No. of classes before CT-2 (3 Weeks)
Midterm Examination	7 September - 21 September 2024 (2 Weeks) No. of classes before Midterm (8 Weeks)
Class Starting After Midterm Exam	22 September 2024
Eid-ul-Fitr Vacation*	--
Class Test - 3	4th Week of October No. of classes before CT-3 (5 Weeks)
Final Examination	3 December –17 December 2024 (2 Weeks) No. of classes before Final (10 Weeks)
Last date of Grade Submission	21 December 2024
Eid-ul-Adha Vacation*	--
Publication of Semester Results	24 December 2024
Semester Break	26 December -29 December 2024
Course File Preparation and Faculty Training	30 December 2024 -5 January 2025
Advising and Registration of Next Semester	1 January - 10 January 2025
Classes Start of Next Semester	11 January 2025

*Dependent on moon sighting

* The schedule may be subject to change based on demand or situational factors.

10.1.2024

(Professor Dr. S.M. Mahbub-ul-Haque Majumder)
Pro-Vice Chancellor
Daffodil International University